

SOC Design

Week 5: Interfaces



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SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

Arayüzler

Sıklıkla kullanılan kontrol arayüzleri

- UART
- SPI
- I2C

SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

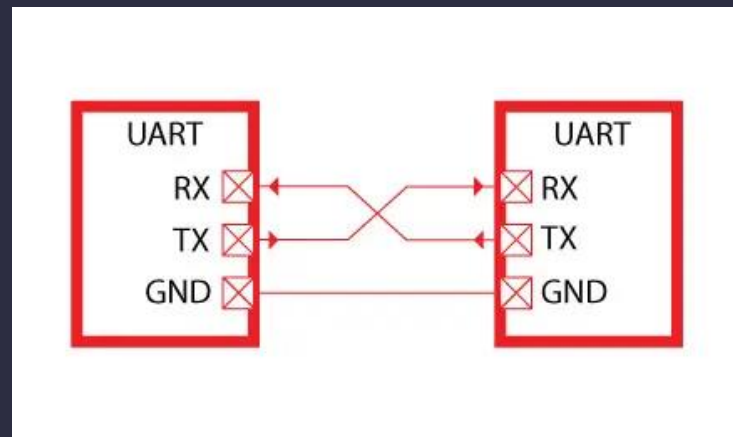
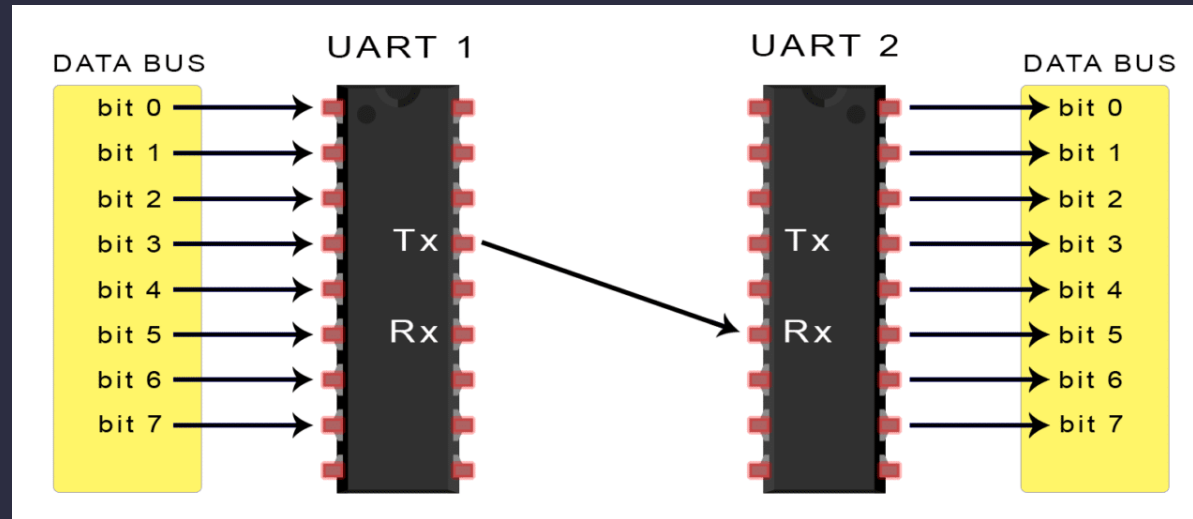
Arayüzler

Sıklıkla kullanılan yüksek bantgenişliğine sahip arayüzler

- PCIe
- Ethernet
- USB

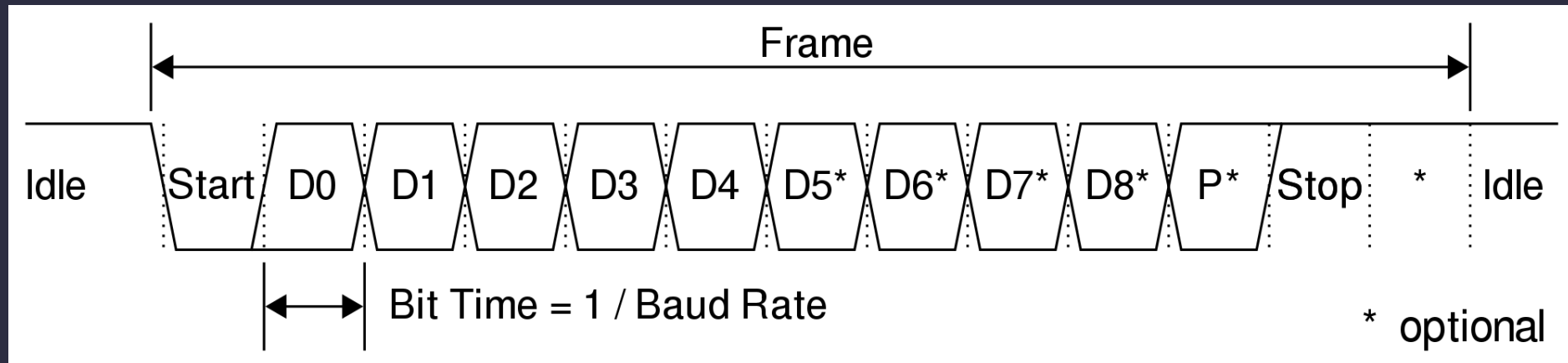
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UART



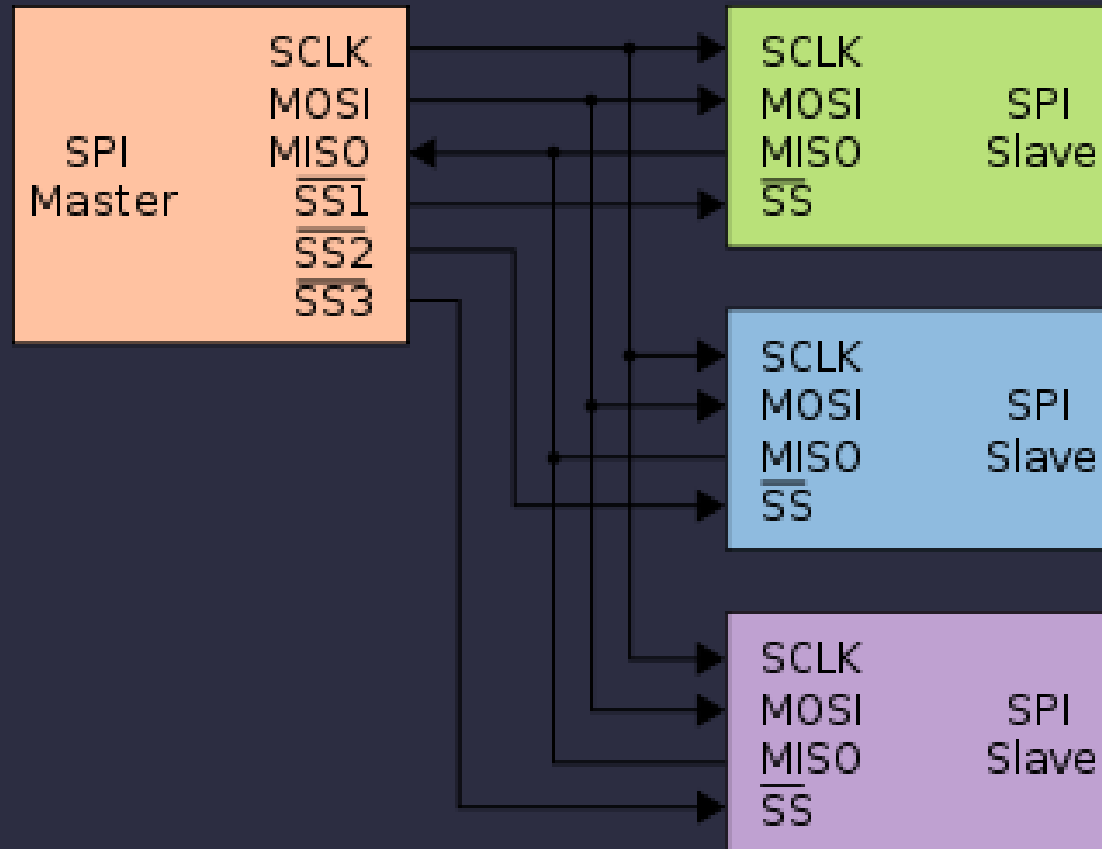
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UART



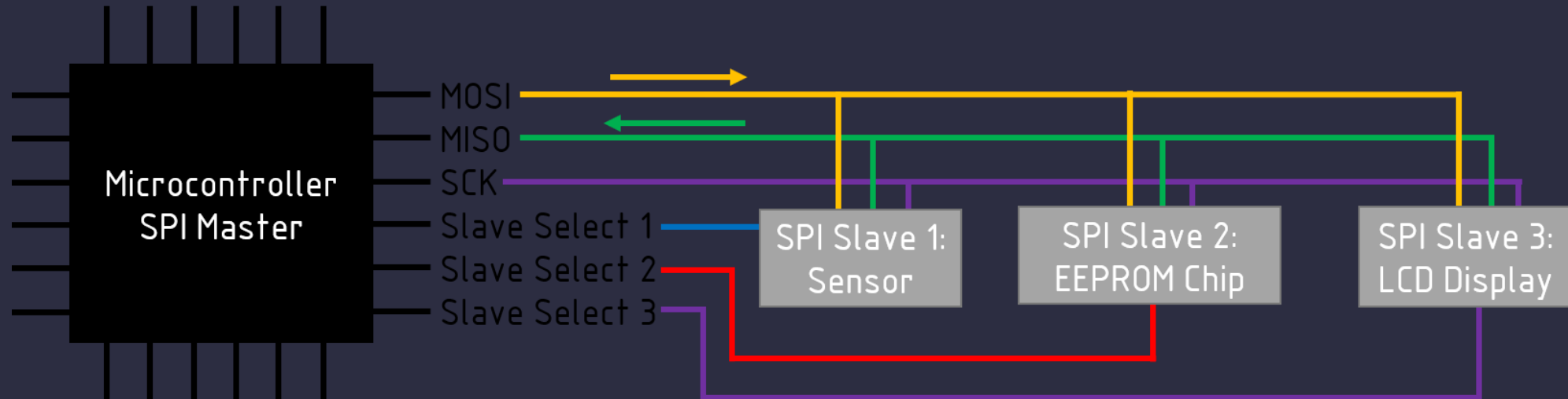
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SPI



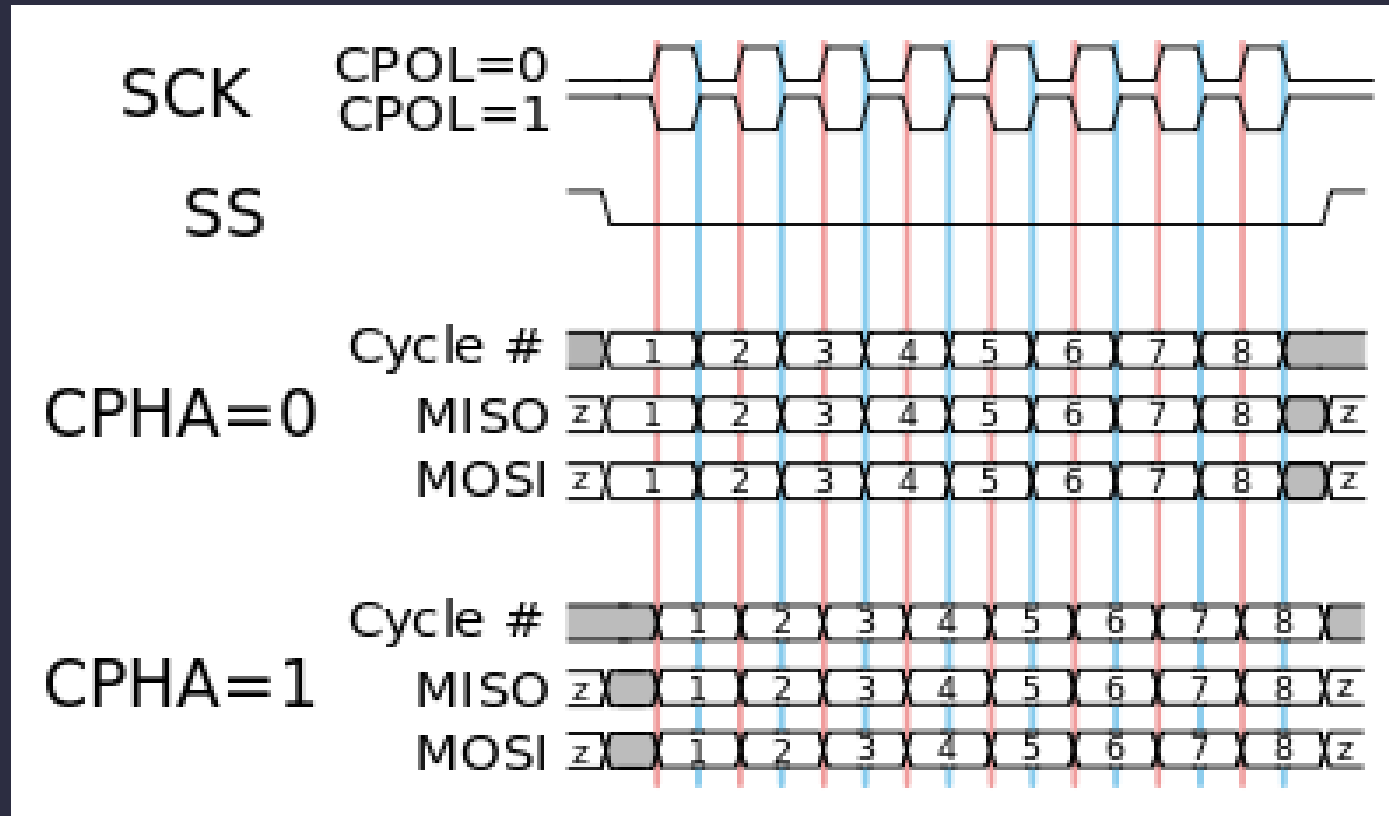
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SPI



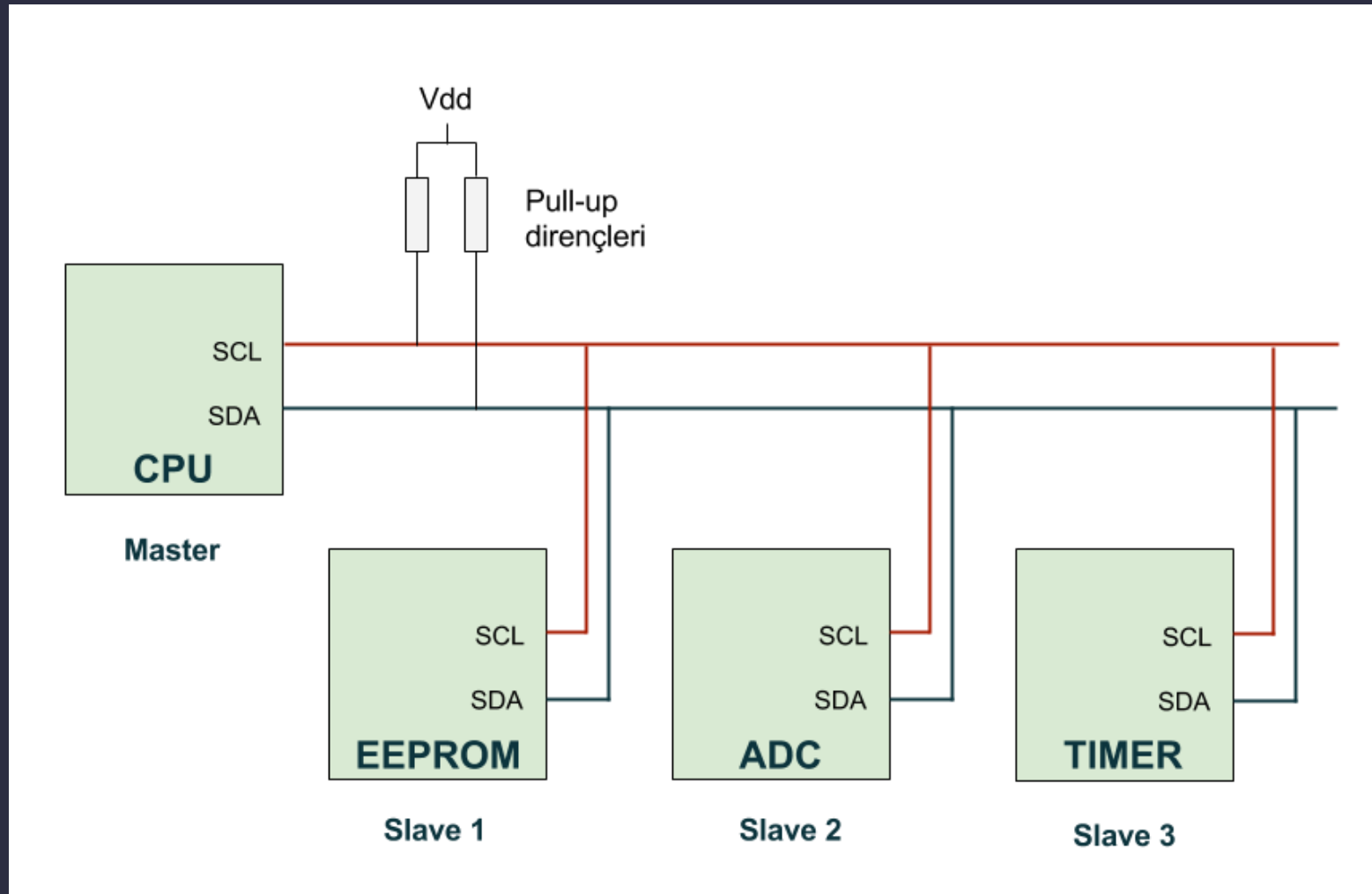
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SPI



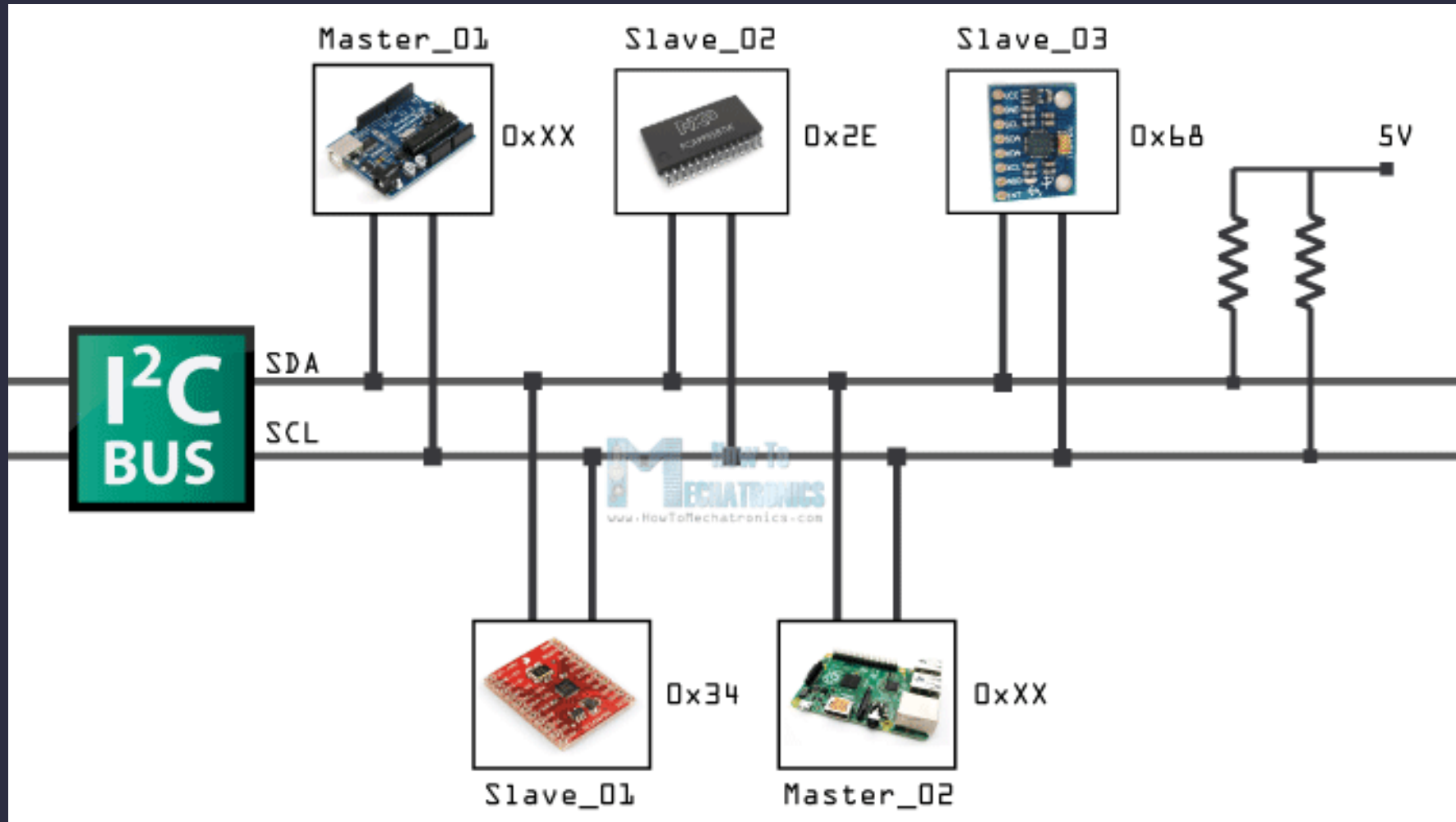
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I2C



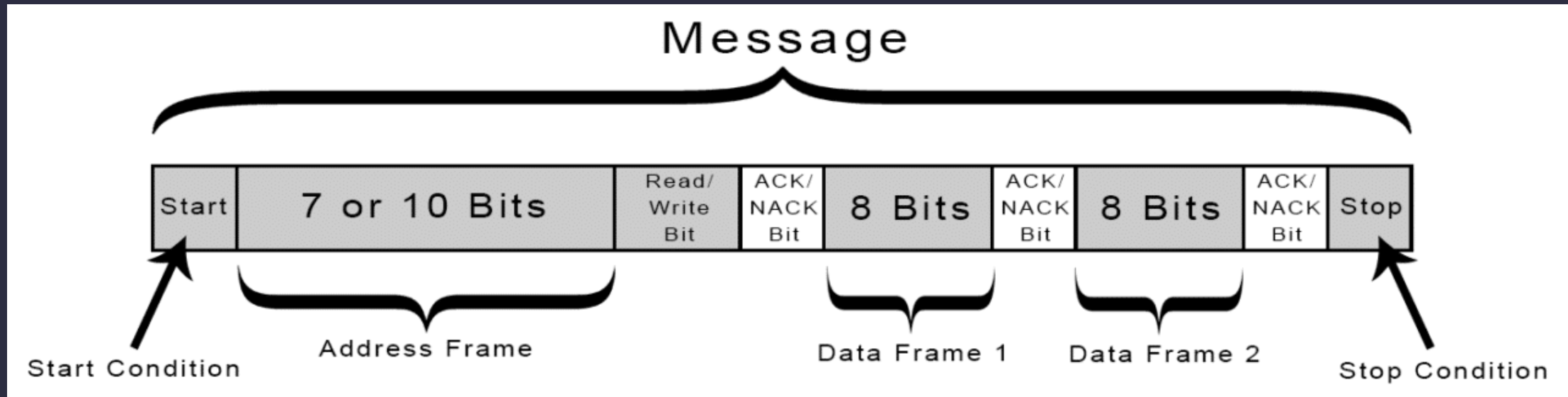
SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

I2C



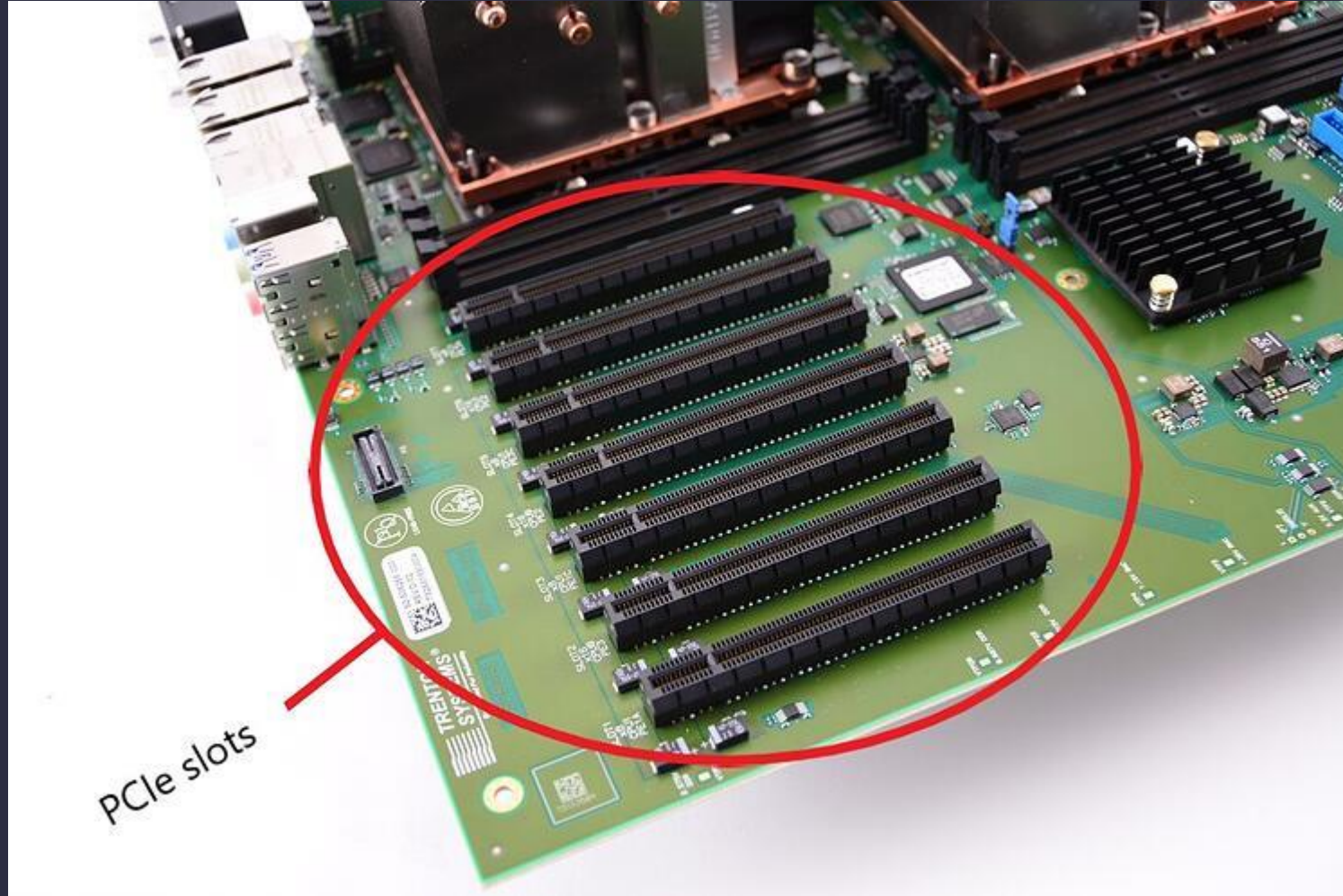
SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

I2C



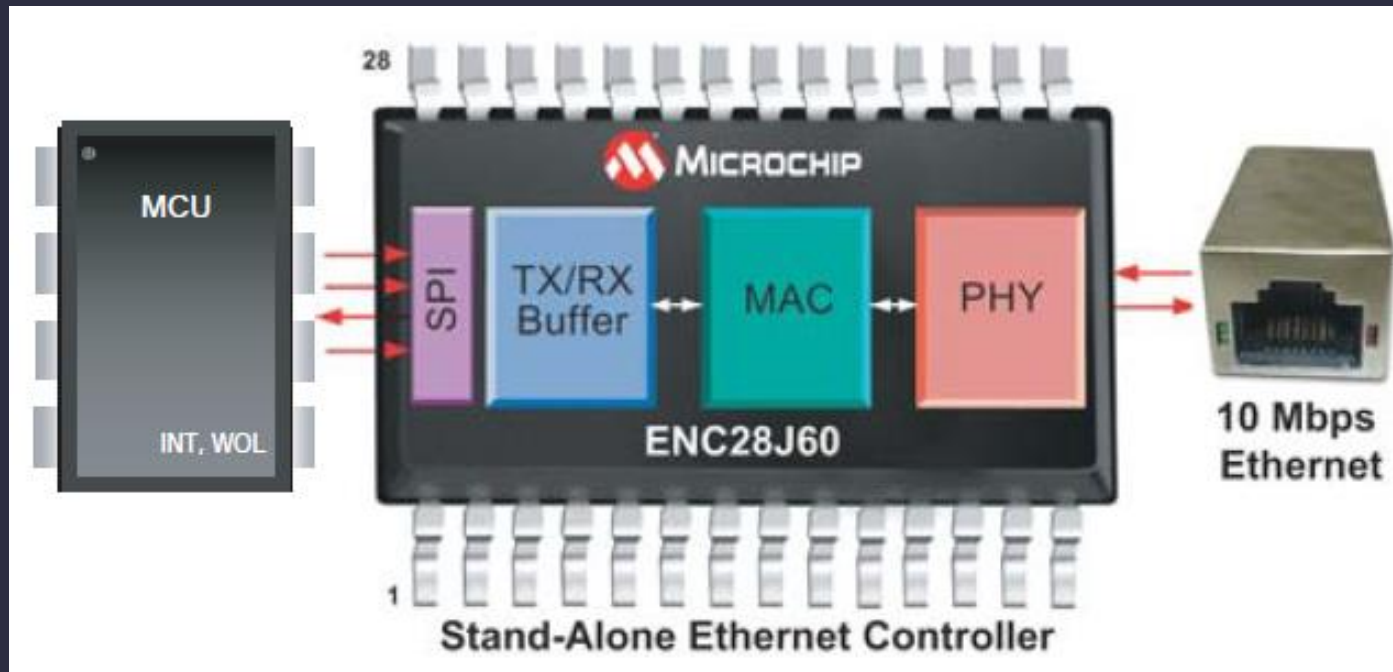
SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

PCIe



SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

Ethernet



SOC Konseptleri, Xilinx IPI Tasarım ve Arayüzler

USB

